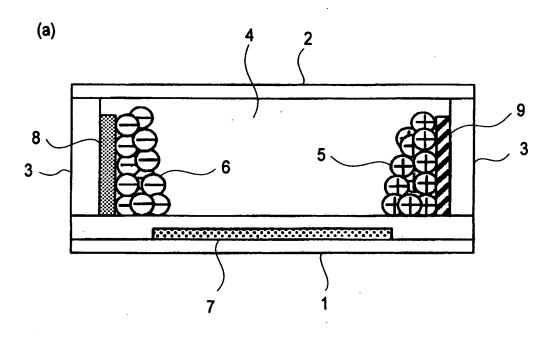


FIG.1



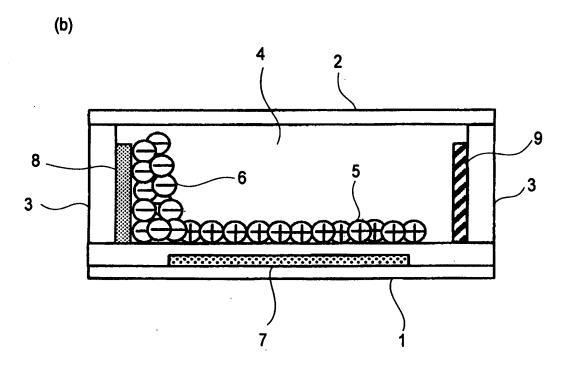
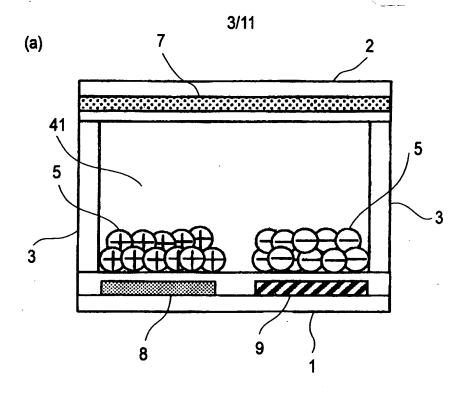


FIG.2

U.S. PATENT APPLICATION
INVENTOR: YOJIRO MATSUDA
TITLE: ELECTROPHORETIC DISPLAY
PPARATUS AND DRIVING METHOD
HEREOF
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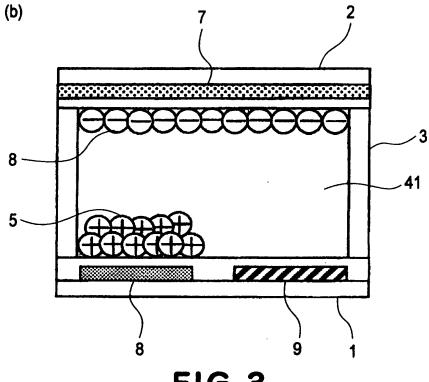
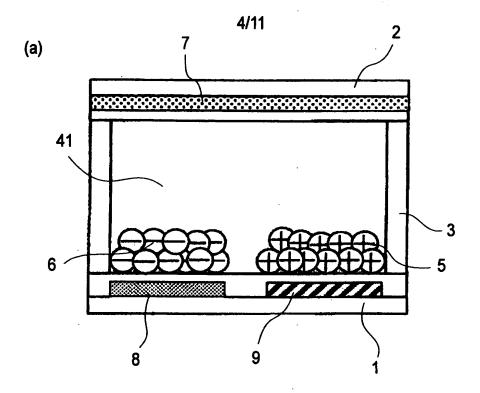
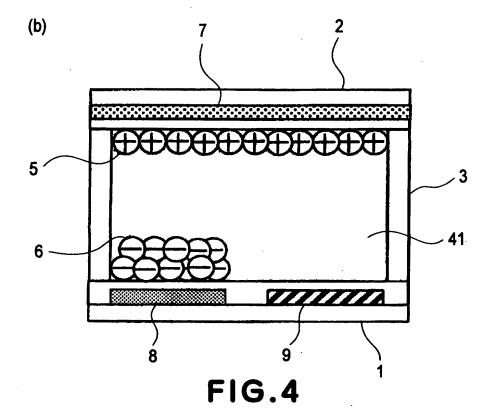
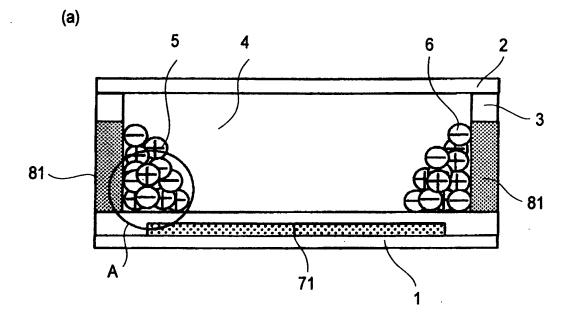


FIG.3

U.S. PATENT APPLICATION
INVENTOR: YOJIRO MATSUDA
TITLE: ELECTROPHORETIC DISPLAY
PPARATUS AND DRIVING METHOD
HEREOF 10/553954
Docket No.: 00684.102867







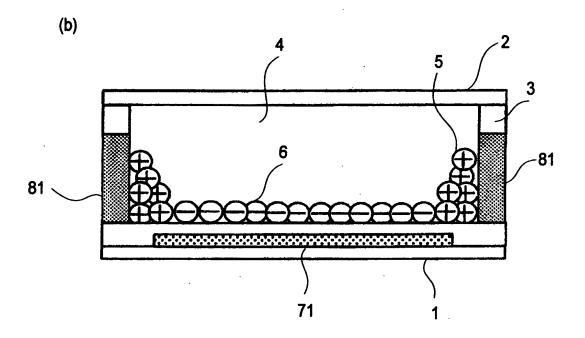
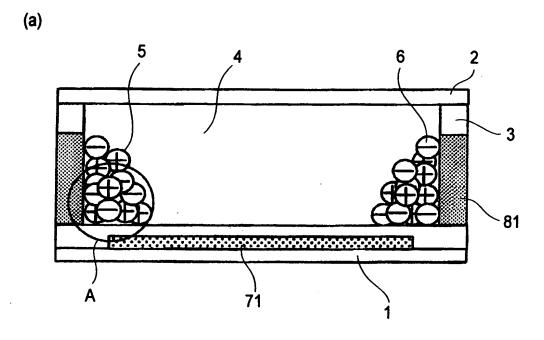


FIG.5



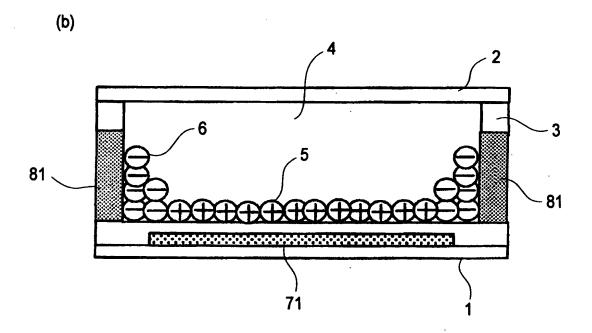


FIG.6

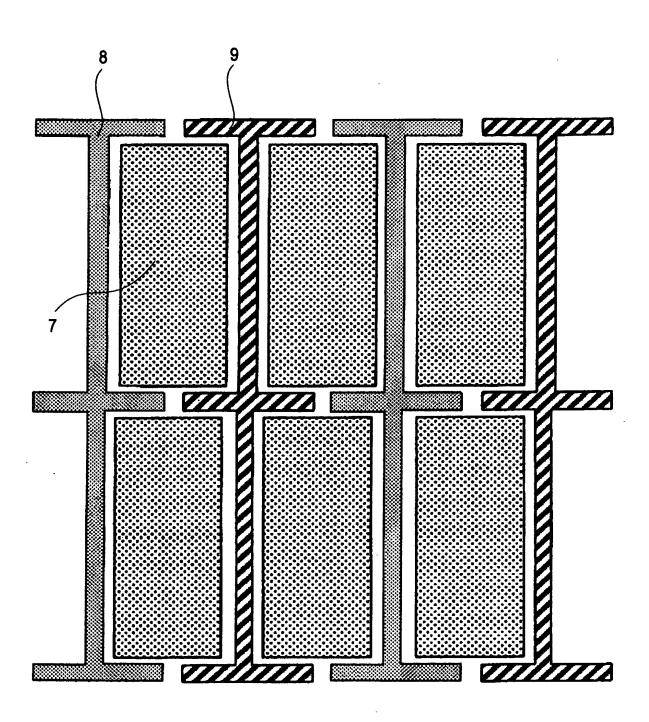


FIG.7

5 15 h

(a)	①	①	①	①	①	1	1	①
	1	①	Θ	①	①	1	①	1
	1	1	1	①	①	①	1	1
	①	①	1	1	1	①	①	①
	1	1	①	①	1	①	①	①
	1	1	①	1	①	1	①	1
	①	1	①	1	①	1	①	①
	①	①	①	①	①	①	①	①

								,
(b)	2	2	2	2	2	2	2	2
	2	2	2	2	2	@	2	2
	2	2	2	@	@	@	2	2
	2	@	2	2	2	2	2	2
	2	2	2	2	2	2	2	2
	2	2	2	2	2	2	2	2
	2	2	2	2	2	2	2	2
	2	2	2	2	2	2	2	2

FIG.8

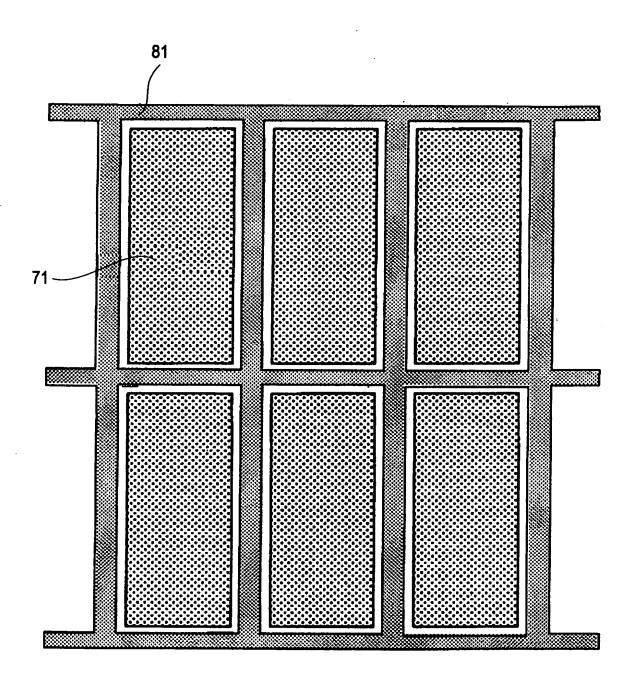


FIG.9

(a)	1	1	1	1	①	1	1	1
	2	2	2	2	2	2	2	2
	1	①	①	①	1	1	1	①
	2	2	2	2	2	2	2	2
	1	①	①	1	1	1	①	①
	2	2	2	2	2	2	2	2
	①	1	1	①	1	①	①	①
	2	2	2	2	2	2	2	2

		_r	,	<i>-</i>				
(b)	2	2	2	2	2	2	2	2
	①	①	①	1	①	1	①	①
	2	2	2	2	2	2	2	2
	1	①	①	①	①	①	①	①
	2	2	2	2	2	2	2	2
İ	1	①	①	1	①	①	1	1
	2	2	2	2	2	2	2	2
	1	①	①	1	①	①	1	1

FIG.10

U.S. PATENT APPLICATION
INVENTOR: YOJIRO MATSUDA
TILE: ELECTROPHORETIC DISPLAY
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(a)

①	2	1	2	1	2	1	2
2	①	2	1	2	1	2	①
①	2	1	2	①	2	1	2
2	1	@	①	2	1	2	1
①	2	①	2	①	2	①	2
2	①	2	①	2	①	@	①
①	2	①	2	①	2	①	2
2	①	2	①	2	①	2	①

(b)

2	①	2	1	2	①	2	1
1	2	①	@	1	2	①	2
2	1	2	①	2	1	2	①
①	2	①	@	1	2	1	2
2	1	2	①	2	①	@	1
1	2	①	2	①	@	①	2
2	1	2	1	@	1	2	①
①	2	①	2	①	2	①	2

FIG.11